

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Divisional application of S/N: 10/438,927:

Yasurou MATSUZAKI

Art Unit: 2824 (expected)

Serial No.: NEW

Examiner: V. Nguyen (expected)

Filed: April 23, 2004

Docket No.: 108066-00105

For: MEMORY CIRCUIT WITH AUTOMATIC PRECHARGE FUNCTION, AND  
INTEGRATED CIRCUIT DEVICE WITH AUTOMATIC INTERNAL  
COMMAND FUNCTION

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 23, 2004

Dear Sir:

Prior to the examination of the above-identified application, please amend the  
above-identified application as follows